Questa 10.3 Series Product Comp	arison		
	Questa Core	Questa Prime	Questa Ultra Advanced Verification,
Feature	Large Block/System Simulation and ABV	Advanced Verification, All Languages	Advanced Verification, All Languages, Fastest Closure
General Licensing - Floating License	•	•	•
Language Neutral License	Option (LNL)	•	•
ASIC Sign-Off Platform-Independent Compiled Database	•	•	•
Native-Compiled Architecture	•	•	•
Incremental Compilation 32/64-Bit Cross-Compatability	■ ■	•	•
Languages	•	-	•
SystemVerilog IEEE1800 Design			
SystemVerilog IEEE1800 Verification VHDL (including VHDL-2008)	Assertions Only VHDL, LNL or Mixed	■ ■	•
Verilog	VLOG, LNL or Mixed	<u> </u>	
Mixed Language	Mixed	• -	• -
PSL (IEEE 1850) Verilog PLI/VPI	■ ■		•
SystemVerilog Direct Programming Interface		•	•
VHDL FLI UPF 1.1 and 2.0 (IEEE 1801)	Option (PA Sim)	Option (PA Sim)	•
SystemC 2.2 (IEEE 1666/OSCI 2.2)	Option (SystemC)	Option (PA Sim) ■	-
Analog/Mixed Signal (Advance MS Product)	Questa ADMS	Questa ADMS	Questa ADMS
Advanced Verification Assertion-Based Verification	—	■	•
Questa Verification Library	OVL and QVLChecker	_	
(Assertion Checker & Monitor Library)	Library Only	•	•
SystemVerilog Constrained-Random Test Generation		_	
SystemVerilog Covergroups (and Coverpoints)		-	-
Open Verification Methodology (OVM)		•	• -
Unified Verification Methodology (UVM) Power Aware Simulation		Option(PA Sim)	•
inFact Testbench Automation		Add On Product	-
Codelink		Add On Product	Add On Product
(Processor-Based Verification) Questa Verification Components			
(Questa Verification IP Library)		Add On Product	Add On Product
Verification Management & Coverage Code Coverage (with Toggle Coverage)	_	_	•
Code Coverage (with Toggle Coverage)	PSL and SVA Cover	<u> </u>	-
Functional Coverage	directive	•	•
Unified Coverage DataBase (UCDB)	Code Coverage and PSL and SVA Assert and Cover	•	•
ominos coronago battabaco (COBB)	Code Coverage and PSL		
Coverage Viewer	and SVA Assert and Cover	•	•
Test Ranking	Code Coverage and PSL and SVA Assert and Cover	_	_
Test Plan Tracking with Excel/OpenOffice plugin	Option (VM)	<u> </u>	-
Verification Results Analysis and database	Option (VM)		
Verification Run Management (regressions)	Option (VM)	•	•
Design Entry, Debug, and Analysis HDL Editor	■	■	•
Integrated Project Manager			
Source Code Templates and Wizards Interactive & Post-Simulation Debug	■ ■	• •	•
Dataflow Graphical & Textual Causality Traceback	-	<u> </u>	-
Schematic viewer	•		
Advanced FSM Debug Source Annotation	•	=	• •
Hyperlinked Navigation	•	- -	-
Class Browser			
Dynamic Thread Debug & Control	■		-
OVM/UVM - Aware Debugging Assertion Thread Viewer	■3	•	•
C Debugger	Option ¹	-	_ _
Memory Window	•	•	• -
Multiple Waveform Windows Extra Standalone Viewer	■ Option	■ Option	■ Option
Waveform Compare	- Φιίοπ 	Option ■	Option ■
Transaction Viewing and Analysis	Option ¹	•	
Protocol Recognition with transactions	_	Option ²	Option ²
JobSpy SignalSpy	■ ■	•	•
User-Customizable GUI (via Tk)	•	•	•
Simulation Single-Kernel Simulation Engine	—	•	•
Verilog RTL & Gate Performance Optimizations	■ ■	- -	- -
VHDL RTL & VITAL Performance Optimizations	•	•	•
Performance and Memory Profiler Dynamic Object Capacity Profiler	•	•	•
Simulation & Compile Flow Optimizations	■	- -	- -
Waveform Management Tool Set	•		
VCD and Extended VCD Support VCD Re-Simulation		•	•
Batch Mode Simulation	■ ■	•	•
Integrated Sim Farm Support (via JobSpy)			
Interactive Simulation Checkpoint & Restore	■ ■	•	•
VHDL 2008 Encryption	■ ■	<u> </u>	- -
Verilog 2005 Encryption	•	•	
SecureIP Platform Support		•	•
папотпі виррогі			
32-Bit OS Support	Linux, Windows XP/Vista,	Linux, Windows XP/Vista,	Linux
Joe Dit Go Support	Win7,8 ⁴	Win7,8 ⁴	LIIIUA
C4 D# OC Comment			15 00.01
64-Bit OS Support	Linux x86-64, Win7,8-64 ⁵	Linux x86-64, Win7,8-64 ⁵	Linux x86-64
1 - Included in SystemC Option	4 - LINUX RHEL 4,5,6 SLES 10,11 Wind	ows 32 XP Vieta Windows 7	
	57 1,0,0 OLLO 10,11 WIND	, riota, rrindows /	

2 - Included with Questa Verification IP

5 - LINUX RHEL 4,5,6 SLES 10,11 Windows 64 Windows 7 3 - OVM and UVM Aware Debugger is present but things like randomize, and functional coverage for OVM/UVM are not available

NOTE - not available on Windows: wlf threads, vopt threads, multi-core

MS 10.3a

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